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Memory Technology, Design and Testing, 2004. Records of the 2004 Internatic
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Digital Object Identifier 10.1109/MTDT.2004.1327979

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Semantics, Knowledge and Grid, 2005. SKG '05. First International Conferenc
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□   3. **Emulating Web cache replacement algorithms versus a real system**
Cardenas, L.G.; Gil, J.A.; Sahuquillo, J.; Pont, A.;
Computers and Communications, 2005. ISCC 2005. Proceedings. 10th IEEE ≤
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□   4. **A space-efficient caching mechanism for flash-memory address translati**
Chin-Hsien Wu; Tei-Wei Kuo; Chia-Lin Yang;
Object and Component-Oriented Real-Time Distributed Computing, 2006. ISO
IEEE International Symposium on
24-26 April 2006 Page(s):8 pp.
Digital Object Identifier 10.1109/ISORC.2006.13

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□   5. **Impact of Technology Scaling on Energy Aware Execution Cache-Based Microarchitectures**
Talpes, E.; Marculescu, D.;
Low Power Electronics and Design, 2004. ISLPED '04. Proceedings of the 200
Symposium on
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6. **Lookahead scheduling requests for multisize page caching**
Kiniwa, J.; Hamada, T.; Mizoguchi, D.;
Computers, IEEE Transactions on
Volume 50, Issue 9, Sept. 2001 Page(s):972 - 983
Digital Object Identifier 10.1109/12.954511

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7. **A data replica replacement algorithm based on value model in mobile gri**
Xiaodong Yan; Huimin Xu; Yajing Xu; Lianxi Liu;
Mobile Technology, Applications and Systems, 2005 2nd International Confere
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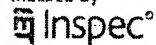**Wed, 3 Jan 2007, 1:09:56 PM EST**

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